

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney Docket No. 10559-0303US1	Application No. 10/069,306
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date July 3, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A1	5,787,454	07/1998	Rohlman			
	A2	5,822,619	10/1998	Sidwell			
	A3	5,859,789	01/1999	Sidwell			
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Examiner Signature /Robert Fennema/	Date Considered 10/26/2009
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	A28	WO 92/07335	04/1992	WIPO				

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	A30	Fiske et al., "Thread prioritization: a thread scheduling mechanism for multiple-context parallel processors", IEEE Comput. Soc., pgs. 210-221, (1995).
	A31	Heuring et al., Computer Systems Design and Architecture, Reading, MA, Addison Wesley Longman, Inc., pp. 69-71, (1997).
	A32	Kane, Gerry, PA-RISC 2.0 Architecture, 1995, Prentice Hall PTR, pp. 1-6, 7-13 & 7-14.
	A33	Sproull, R.F., et al., "The Counterflow Pipeline Processor Architecture", IEEE Design & Test of Computers, pp. 48-59, (1994).

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